45.   (New) The invention of Claim 1, wherein N=1.

46.   (New) The invention of Claim 19, wherein N=1.

47.   (New) A method for creating a write-once memory device from a write-many memory device, the method comprising:

(a)   providing a memory device comprising a memory array comprising a plurality of write-many memory cells; and

(b)   rendering at least some of the write-many memory cells in the memory array as write-once memory cells by preventing more than one write to said at least some of the write-many memory cells.

48.   (New) The invention of Claim 47, wherein the memory device comprises a modular memory device that is removably couplable with a host device.

49.   (New) The invention of Claim 47, wherein said at least some of the write-many memory cells in the memory array are rendered as write-once memory cells by a manufacturer of the memory device.

50.   (New) The invention of Claim 47, wherein the memory array is organized into blocks of memory, and wherein a sideband field of a block is used in preventing more than one write to said at least some of the write-many memory cells.